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** CONTINUING DATA *****

This application is a DIV of 09/348,632 07/06/1999 PAT 6,709,562
 which is a CIP of 08/670,200 06/21/1996 ABN
 which claims benefit of 60/009,538 12/29/1995

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 05/06/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature: <i>[Signature]</i> Initials: <i>[Initials]</i>	US	13	27	1

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TITLE

Electroplated interconnection structures on integrated circuit chips

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